# 10/085369 10/085369 10/085369

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hee-Jueng Lee; Ki-Chang Yoon

Serial No.: To Be Assigned Filed: Concurrently Herewith

For: METHODS OF FABRICATING READ ONLY MEMORY DEVICES INCLUDING

THERMALLY OXIDIZED TRANSISTOR SIDEWALLS, AND DEVICES SO

**FABRICATED** 

February 28, 2002

Box PATENT APPLICATION Commissioner for Patents Washington, DC 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of the identified document. It is requested that this document be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,

Mitchell S. Bigel

Registration No. 29,614

## **Correspondence Address:**

20792

PATENT TRADEMARK OFFICE

### CERTIFICATE OF EXPRESS MAILING

Express Mail Label No. EL 920740544 US

Date of Deposit: February 28, 2002

Thereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: BOX PATENT APPLICATION, Commissioner for Patents. Washington, DC 20231.

Susan E. Freedman

Date of Signature: February 28, 2002